

**FIG. 1**

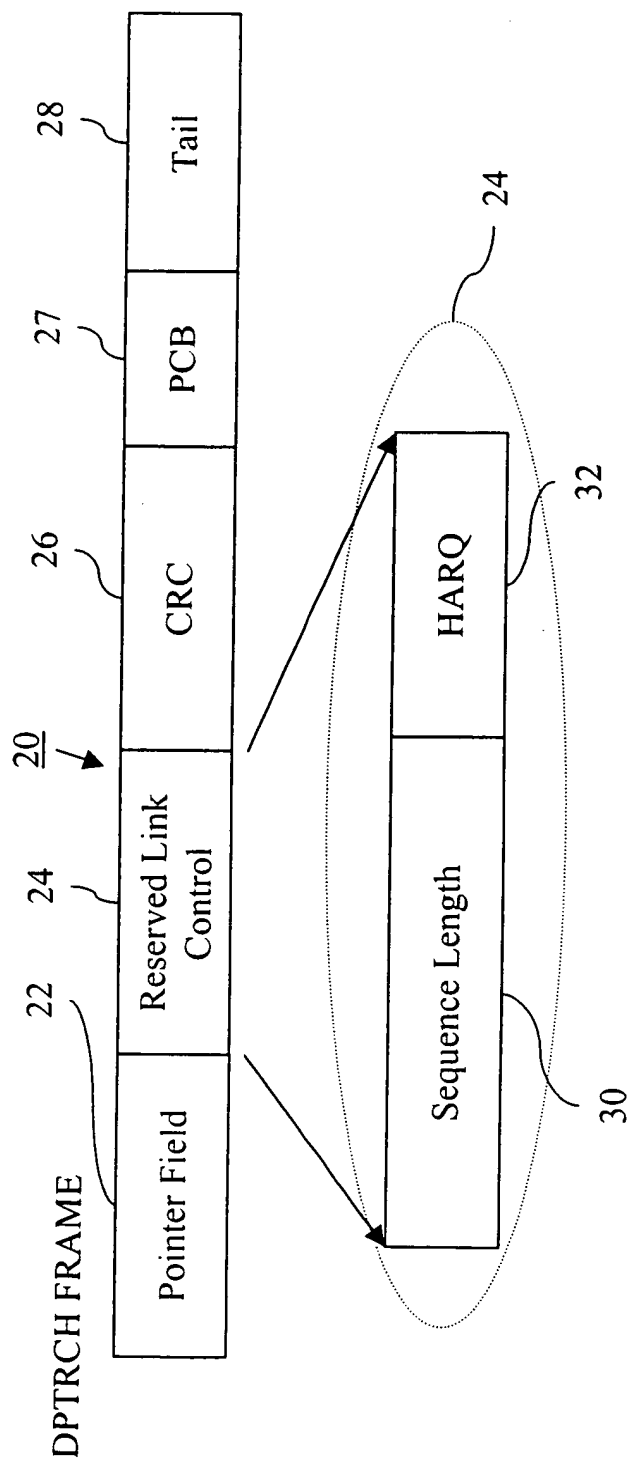


FIG. 2

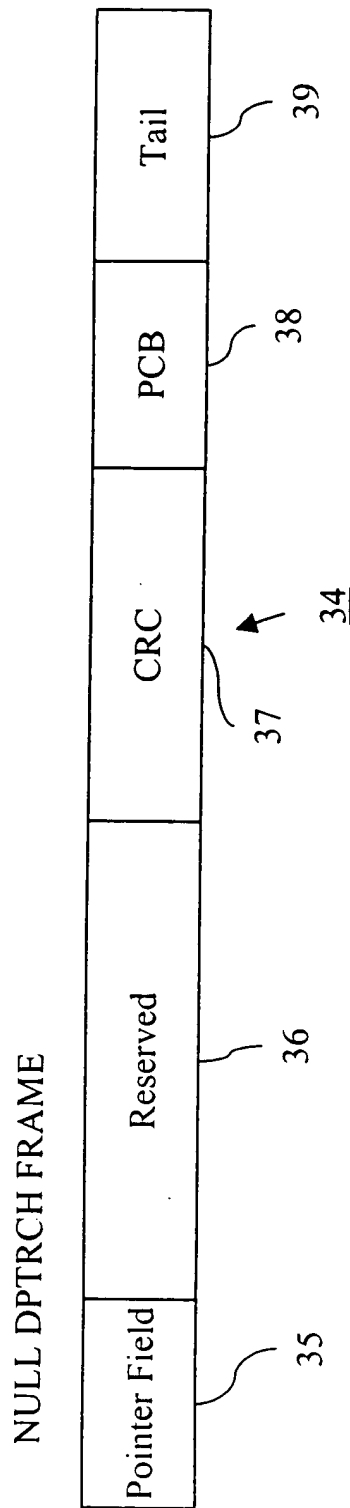


FIG. 3

SHCCH FRAME 42

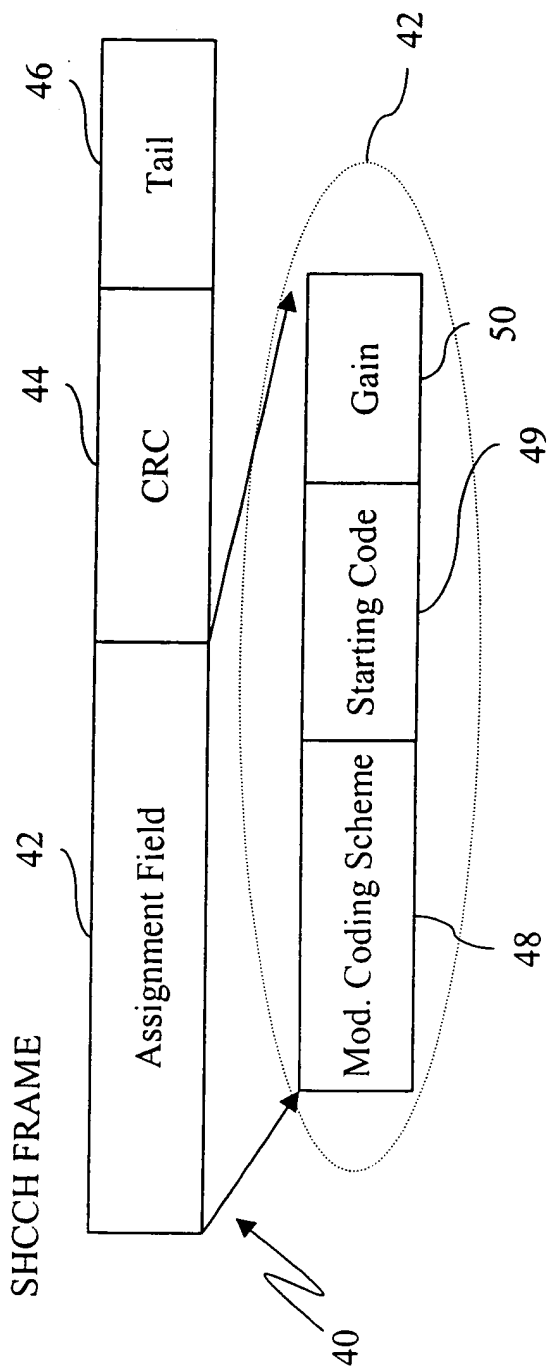
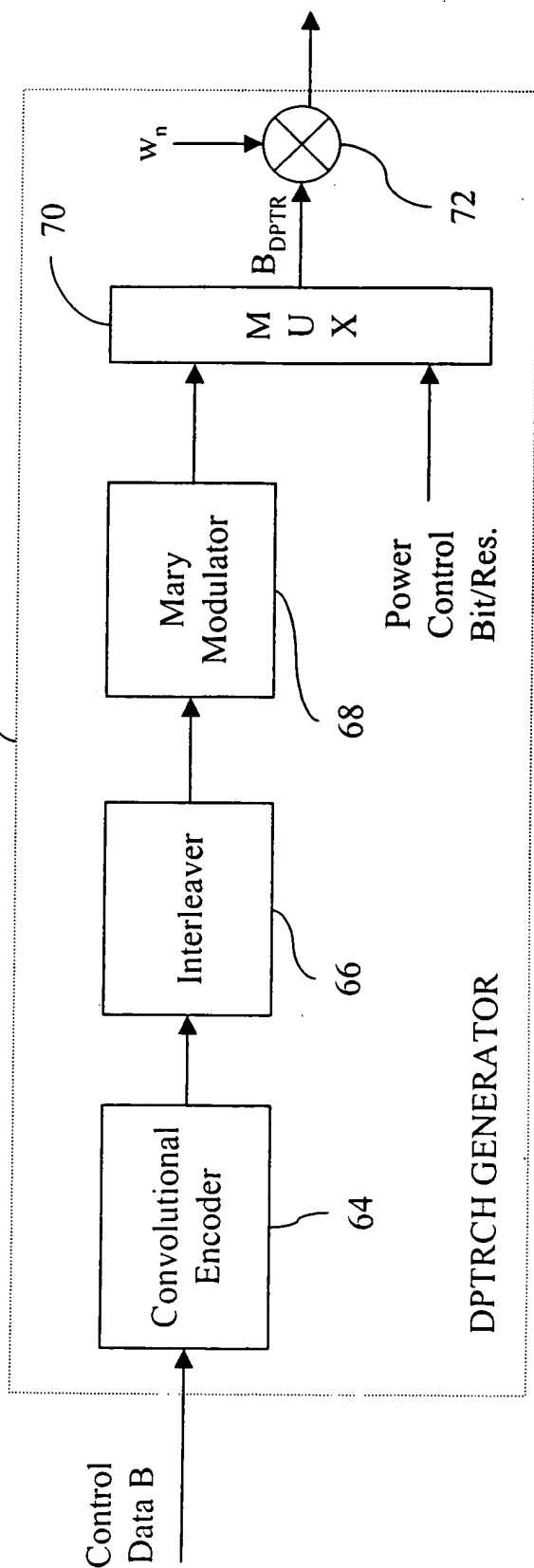


FIG. 4

FIG. 5 is a block diagram of a system for generating DPTRCH and SHCCH signals.

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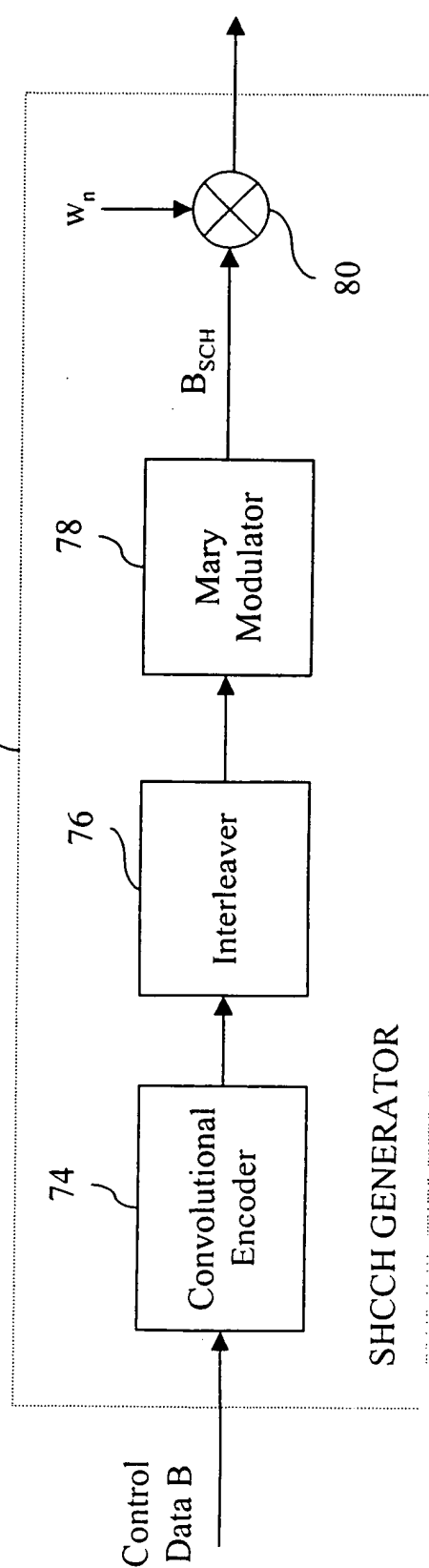


FIG. 5